

L Number	Hits	Search Text	DB	Time stamp
1	3	PERROTT-MICHAEL-H.IN.	USPAT	2003/10/01 09:07
2	3	PERROTT-MICHAEL-H.IN.OR SILICON-LABORATORIES.AS.	USPAT	2003/10/01 09:07
3	3	PERROTT-MICHAEL-H.IN. OR SILICON-LABORATORIES.AS.	USPAT	2003/10/01 09:08
4	0	SILICON-LABORATORIES.AS.	USPAT	2003/10/01 09:08
6	68	SILICON ADJ LABORATORIES.AS.	USPAT	2003/10/01 09:11
5	334429	SILICON AQDJ LABORATORIES.AS.	USPAT	2003/10/01 09:11
7	15	BIT ADJ ERROR ADJ RATE ADJ DETECTION	USPAT	2003/10/01 09:50
8	4	BIT ADJ ERROR ADJ RATE ADJ DETECTION AND DATA ADJ STREAM	USPAT; US-PGPUB; EPO; JPO	2003/10/01 09:49
9	34	BIT ADJ ERROR ADJ RATE ADJ DETECTION	USPAT; US-PGPUB; EPO; JPO	2003/10/01 09:50

✓ 1	Document ID	Issue Date	Pages	title	Current OR
1	<input type="checkbox"/> US 6590426 B2	20030708	36	Digital phase detector circuit and method therefor	327/7
2	<input type="checkbox"/> US 6580376 B2	20030617	35	Apparatus and method for decimating a digital input signal	341/61
3	<input type="checkbox"/> US 6008703 A	19991228	29	Digital compensation for wideband modulation of a phase locked loop frequency synthesizer	332/100
4	<input checked="" type="checkbox"/> US 5943329 A	19990824	21	Code division multiple access mobile communication system	370/335
5	<input checked="" type="checkbox"/> US 5835501 A	19981110	7	Built-in test scheme for a jitter tolerance test of a clock and data recovery unit	714/704
6	<input checked="" type="checkbox"/> US 5764651 A	19980609	12	Bit error rate detection system	714/708
7	<input checked="" type="checkbox"/> US 5550837 A	19960827	13	Adaptive differential pulse code modulation system with transmission error compensation	714/708
8	<input checked="" type="checkbox"/> US 5351245 A	19940927	8	Bit error rate detection method	714/704
9	<input checked="" type="checkbox"/> US 5305323 A	19940419	6	Technique for digitally detecting bit-error densities that occur in a serial bit stream	714/704
10	<input checked="" type="checkbox"/> US 5099500 A	19920324	19	Symbol detection and error correction coding in a local area network	375/340
11	<input checked="" type="checkbox"/> US 5036515 A	19910730	5	Bit error rate detection	714/708

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	327/159; 327/2		Perrott, Michael H.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6590426	<input type="checkbox"/>					
2	327/12; 327/147; 327/156		Perrott, Michael H.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6580376	<input type="checkbox"/>					
3	331/16; 331/23; 375/303		Perrott, Michael H. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6008703	<input type="checkbox"/>					
4	375/141; 375/362		Ohgoshi, Yasuo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5943329	<input type="checkbox"/>
5	714/700; 714/814		Dalmia, Kamal et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5835501	<input type="checkbox"/>
6	714/704		Bullock, Dean C. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5764651	<input type="checkbox"/>
7	714/704		Chang, Hyokang	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5550837	<input type="checkbox"/>
8	714/705; 714/713; 714/715; 714/762		Pregont, Gary J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5351245	<input type="checkbox"/>
9	714/708		Lada, Timothy P.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5305323	<input type="checkbox"/>
10	714/812		Furlong, Darrell	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5099500	<input type="checkbox"/>
11	455/277.1; 714/704		Freeburg, Thomas A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5036515	<input type="checkbox"/>

V	1	Document ID	Issue Date	Pages	Title	Current OR
12	<input checked="" type="checkbox"/>	US 4100531 A	19780711	7	Bit error rate measurement above and below bit rate tracking threshold	375/225
13	<input checked="" type="checkbox"/>	US 6591383 B1	20030708	12	Bit error rate detection	714/704
14	<input checked="" type="checkbox"/>	JP 2000333246 A	20001130	7	RADIO COMMUNICATION SYSTEM	
15	<input checked="" type="checkbox"/>	JP 2000269884 A	20000929	7	RADIO COMMUNICATION SYSTEM	
16	<input checked="" type="checkbox"/>	JP 2000068874 A	20000303	8	RADIO RECEIVER AND ITS INTERMITTENT RECEPTION METHOD	
17	<input checked="" type="checkbox"/>	JP 10004361 A	19980106	9	SEQUENTIAL DECODER	
18	<input checked="" type="checkbox"/>	JP 09331309 A	19971222	9	SEQUENTIAL DECODER	
19	<input checked="" type="checkbox"/>	JP 09074578 A	19970318	4	RADIO CALL RECEIVER WITH BIT ERROR RATE DETECTION FUNCTION	
20	<input checked="" type="checkbox"/>	JP 08330992 A	19961213	10	ERROR RATE DETECTION CIRCUIT RECEIVER FOR FM MULTIPLEX BROADCASTING	
21	<input checked="" type="checkbox"/>	JP 06062067 A	19940304	10	DELAY DETECTION CIRCUIT	
22	<input checked="" type="checkbox"/>	JP 02055435 A	19900223	5	COMMON INTEGRATION METHOD	
23	<input checked="" type="checkbox"/>	EP 400983 A2	19901205	5	Bit error rate detection.	
24	<input checked="" type="checkbox"/>	EP 980161 A1	20000216	19	DIGITAL DATA RECEIVER	
25	<input checked="" type="checkbox"/>	WO 2079988 A1	20021010	22	METHODS AND APPARATUS FOR BURST TOLERANT EXCESSIVE BIT ERROR RATE ALARM DETECTION AND CLEARING	

Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
12		Fletcher, James C. Administrator of the National Aeronautics and Space et al.	<input type="checkbox"/>	US 4100531	<input type="checkbox"/>						
13	714/802	Michel, David et al.	<input type="checkbox"/>	US 6591383	<input type="checkbox"/>						
14		NUMAGAMI, KOICHI	<input type="checkbox"/>	JP 2000333246 A	<input type="checkbox"/>						
15		NUMAGAMI, KOICHI	<input type="checkbox"/>	JP 2000269884 A	<input type="checkbox"/>						
16		YOSHIDA, KOTARO	<input type="checkbox"/>	JP 2000068874 A	<input type="checkbox"/>						
17		YAGI, TOSHIHARU	<input type="checkbox"/>	JP 10004361 A	<input type="checkbox"/>						
18		YAGI, TOSHIHARU	<input type="checkbox"/>	JP 09331309 A	<input type="checkbox"/>						
19		TAKAOKA, YASUHARU	<input type="checkbox"/>	JP 09074578 A	<input type="checkbox"/>						
20		KIMURA, KAZUHIRO et al.	<input type="checkbox"/>	JP 08330992 A	<input type="checkbox"/>						
21	375/329	AZUMA, TAKASHI et al.	<input type="checkbox"/>	JP 06062067 A	<input type="checkbox"/>						
22	714/795	KURODA, YUJI	<input type="checkbox"/>	JP 02055435 A	<input type="checkbox"/>						
23	455/277.2;	FREEBURG, THOMAS A	<input type="checkbox"/>	EP 400983 A2	<input type="checkbox"/>						
24	714/704	HASHIMOTO, AKINORI et al.	<input type="checkbox"/>	EP 980161 A1	<input type="checkbox"/>						
25		SOLTYSIAK, EDWARD et al.	<input type="checkbox"/>	WO 2079988 A1	<input type="checkbox"/>						